

**4W AUDIO AMPLIFIER WITH DC VOLUME CONTROL****TDA1013A**

The TDA1013A is a monolithic integrated audio amplifier circuit with d.c. volume control in a 9-lead single in-line (SIL) plastic package. The wide supply voltage range makes this circuit very suitable for applications in mains-fed apparatus such as television receivers and record players.

The d.c. volume control stage has a logarithmic control characteristic with a range of more than 80 dB; control can be obtained by means of a variable d.c. voltage between 3.5 and 8 V.

The audio amplifier has a well defined open loop gain and a fixed integrated closed loop gain. This offers an optimum in number of external components, performance and stability.

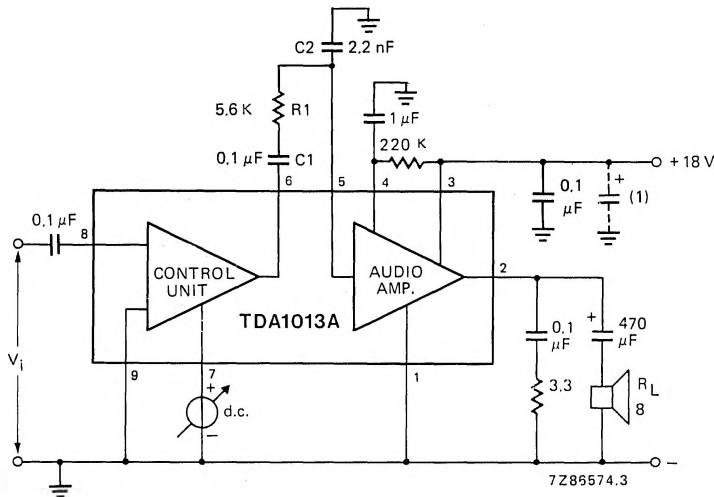
The SIL package (SOT-110B) offers a simple and low-cost heatsink connection.

**QUICK REFERENCE DATA**

Supply voltage range	$V_P$	15 to 35 V
Repetitive peak output current	$I_{ORM}$	max. 1.5 A
Total sensitivity (d.c. control at max. gain) for $P_O = 2.5$ W	$V_i$	typ. 55 mV
<b>Audio amplifier</b>		
Output power at $d_{tot} = 10\%$ $V_P = 18$ V; $R_L = 8 \Omega$	$P_O$	typ. 4.5 W
Total harmonic distortion at $P_O = 2.5$ W; $R_L = 8 \Omega$	$d_{tot}$	typ. 0.5 %
Sensitivity for $P_O = 2.5$ W	$V_i$	typ. 125 mV
<b>D.C. volume control unit</b>		
Gain control range	$\phi$	> 80 dB
Signal handling at $d_{tot} < 1\%$ (d.c. control at 0 dB)	$V_i$	> 1.2 V
Sensitivity for $V_O = 125$ mV at max. voltage gain	$V_i$	typ. 55 mV
Input impedance (pin 8)	$ Z_i $	typ. 250 k $\Omega$

**PACKAGE OUTLINE**

9-lead SIL; plastic (SOT-110B).



(1) Belongs to power supply.

Fig. 1 Basic application diagram also used as test circuit with  $R_1 = 5.1 \text{ k}\Omega$  and  $C_1 = 22 \text{ nF}$ .

### RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Supply voltage	$V_p$	max.	35 V
Non-repetitive peak output current	$I_{OSM}$	max.	3 A
Repetitive peak output current	$I_{ORM}$	max.	1.5 A
Storage temperature	$T_{stg}$		-55 to +150 °C
Crystal temperature	$T_j$		-25 to +150 °C
Total power dissipation			see derating curve Fig. 2

### HEATSINK DESIGN

Assume  $V_p = 18 \text{ V}$ ;  $R_L = 8 \Omega$ ;  $T_{amb} = 60 \text{ }^\circ\text{C}$  (max.);  $T_j = 150 \text{ }^\circ\text{C}$  (max.); for a 4 W application into an  $8 \Omega$  load, the maximum dissipation is about 2.5 W.

The thermal resistance from junction to ambient can be expressed as:

$$R_{th \text{ j-a}} = R_{th \text{ j-tab}} + R_{th \text{ tab-h}} + R_{th \text{ h-a}} = \frac{T_{j \text{ max}} - T_{amb \text{ max}}}{P_{\text{max}}} = \frac{150 - 60}{2.5} = 36 \text{ K/W.}$$

Since  $R_{th \text{ j-tab}} = 9 \text{ K/W}$  and  $R_{th \text{ tab-h}} = 1 \text{ K/W}$ ,  $R_{th \text{ h-a}} = 36 - (9 + 1) = 26 \text{ K/W}$ .

## 4W AUDIO AMPLIFIER WITH DC VOLUME CONTROL

TDA1013A

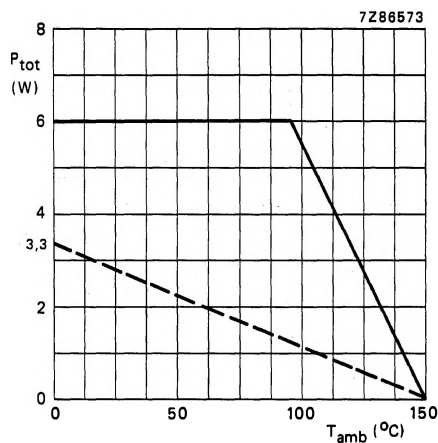


Fig. 2 Power derating curve.  
 ——— infinite heatsink;  
 - - - without heatsink.

## CHARACTERISTICS

$V_p = 18\text{ V}$ ;  $R_L = 8\ \Omega$ ;  $f = 1\text{ kHz}$ ;  $T_{\text{amb}} = 25\text{ }^\circ\text{C}$ ; unless otherwise specified

Supply voltage	$V_p$	typ. 18 V 15 to 35 V
Total quiescent current	$I_{\text{tot}}$	typ. 35 mA
Noise output voltage (see also note)	$V_n$	< 1.4 mV
Total sensitivity (d.c. control at maximum gain) for $P_O = 2.5\text{ W}$	$V_i$	38 to 69 mV typ. 55 mV
Frequency response (-3 dB)	$f$	35 Hz to 20 kHz
<b>Audio amplifier</b>		
Repetitive peak output current	$I_{\text{ORM}}$	< 1.5 A
Output power at $d_{\text{tot}} = 10\%$	$P_O$	> 4 W typ. 4.5 W
Total harmonic distortion at $P_O = 2.5\text{ W}$	$d_{\text{tot}}$	typ. 0.5 % < 1 %
Voltage gain	$G_v$	typ. 30 dB
Sensitivity for $P_O = 2.5\text{ W}$	$V_i$	typ. 125 mV
Input impedance (pin 5)	$ Z_i $	> 100 k $\Omega$ typ. 250 k $\Omega$

## Note

Measured in a bandwidth according to IEC 179-curve 'A';  $R_S = 5\text{ k}\Omega$  and d.c. control at minimum gain.

**4W AUDIO AMPLIFIER WITH DC VOLUME CONTROL****TDA1013A****CHARACTERISTICS (continued)****D.C. volume control unit**

Gain control range (see also Fig. 3)

$$\phi > 80 \text{ dB}$$

Signal handling at  $d_{\text{tot}} < 1\%$   
(d.c. control at 0 dB)

$$V_i > 1.2 \text{ V}$$

Sensitivity for  $V_O = 125 \text{ mV}$  at max. voltage gain

$$V_i \text{ typ. } 55 \text{ mV}$$

Input impedance (pin 8)

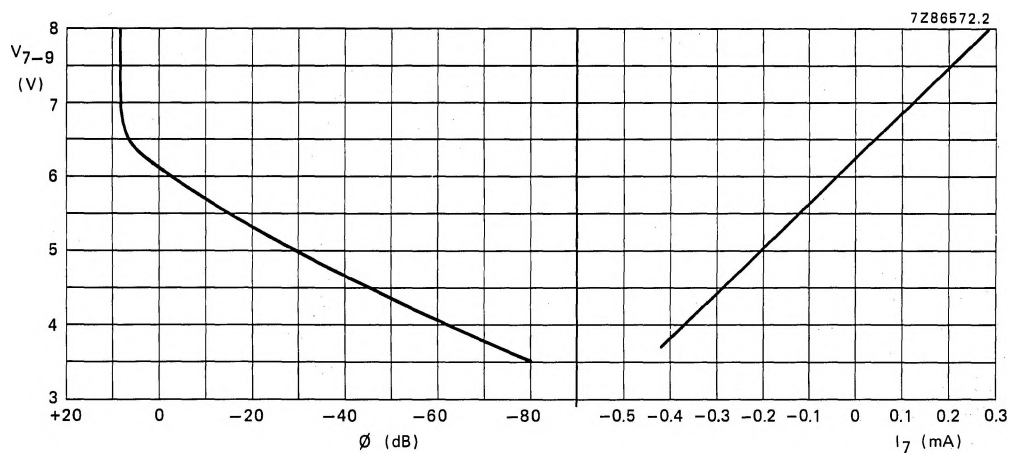
$$|Z_i| > 100 \text{ k}\Omega$$

$$\text{typ. } 250 \text{ k}\Omega$$

Output impedance (pin 6)

$$|Z_O| \text{ } 100 \text{ to } 400 \text{ }\Omega$$

$$\text{typ. } 200 \text{ }\Omega$$

Fig. 3 Typical values gain control;  $V_i$  at pin 7.