MINED BY :		FILE NO . CAS-0006273
Yung Chang Hu	EMERGING DISPLAY	ISSUE : OCT.07, 2010
ROVED BY:	TECHNOLOGIES CORPORATION	TOTAL PAGE: 30
David Chang		VERSION : 5
CUSTOMER	ACCEPTANCE SPEC	CIFICATIONS
MO FOR CUSTOMER'S APPROV DATE : BY :	DEL NO.: <u>ET035009DH6</u> (RoHS) MESSRS: 	

EMERG	ING D		MODEL NO.	VERSION	PAGE
_	OGIES CORI	. –	E T 0 3 5 0 0 9 D H 6 (RoHS)	5	0-1
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	REVISED				
DATE	PAGE NO.		S U M M A R Y		
JAN.02, 2008	1		SPECIFICATIONS		
			ZE (inch) : $3.5^{\circ} \rightarrow \text{DIAGONALS}$: 3.5 in 2K (18BIT) $\rightarrow 262$ K, ADD (12) INTERF		
	2		ABSOLUTE MAXIMUM RATINGS	ACE MODE	
		PARAMETER POWER DISSIPATION FORWARD CURRENT	$ \begin{array}{c c c c c c c c c c c c c c c c c c c $		
		REVERSE VOLTAGE PARAMETER	VR		
		LED BACKLIGHT DISSIPATIO LED BACKLIGHT CURRENT LED BACKLIGHT			
	3	4 FLECTRICAL			
		DIGITAL OPER	RATING CURRENT : TYP.= $(1) \rightarrow 1$, MA	• •	
			RATING CURRENT : TYP.=(10) → 10, LTAGE→LED BACKLIGHT FORWA		
			AMBIENT TEMP. VS. ALLOWABLE F		
		(PER LED)			
	4	5. TIMING CHAR ADD	T		
		VDD VDD			
	7				
	7	6.1 OPTICAL CHA			
		VIEWING ANGLE 0_{t+} CONTRAST RATIO CR	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		RESPONSE TIME tr(rise tf(fall THE BRIGHTNESS D	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		
		OF MODULE WHITE X Y	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		COLOR OF RED X CIE COORDINATE GREEN X Y	$\begin{array}{cccccccccccccccccccccccccccccccccccc$		
		BLUE X THE UNIFORMITY OF BRIGHTNESS —	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		I T E M SYMBO	DL CONDITION MIN. TYP. MAX. UNIT REMARK 0,=0° 50 55 70 75		
		VIEWING ANGLE θ_{x} θ_{x}	$CR \ge 10$ 0.075 $-$ deg. (2), (3) $\theta_{1}=0^{-0}$ 70 75 $-$		
		$\frac{\text{CONTRAST RATIO}}{\text{RESPONSE TIME}} \frac{\text{CR}}{\text{T}_{\text{f}}(\text{fail})}$) 0X-0-; 0y-0 35 50 IISEC (4)		
		OF MODULE B WHITE WX WY	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$		
		COLOR OF RED RX CIE COORDINATE GREEN GX	$\theta_X=0^\circ, \ \theta_Y=0^\circ$ IF=20mA 0.262 0.305 0.355 0.405 0.362 0.362 0.362 0.362 0.362 0.362 0.362 0.362 0.362		
		THE UNIFORMITY OF	N15C '00'0 0.333 0.583 0.633 0.090 0.140 0.190 70 %		
	8	6.2 THE TEST ME	THOD OF BRIGHTNESS AND UNIFC	RMITY	
			V.A		
				6 9	
		6.3 THE CALCUIT	ATING METHOD OF UNIFORMITY	K	
		UNIFORMITY:	MAXIMUN BRIGHTESS-MINIMUN BRIG	HTESS ×100%	\rightarrow
			AVERAGE BRIGHTESS		
		UNIFORMITY: 1	- MAXIMUM BRIGHTNESS-MINIMUM BR AVERAGE BRIGHTNESS	$\left \frac{1GHTNESS}{2} \right \times 10$	00%
	9	7. OUTLINE DIM			
		$MARK \triangle : AD$	DING NOTE		

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JAN.02, 2008	13	IO. INTERFACE S PIN NO SYMBOL 36 HSYNC 37 VSYNC 38 ENB PIN NO SYMBOL 36 HSYNC 36 HSYNC 36 HSYNC 37 VSYNC 38 ENB			
	14	11. POWER SUPP		3.3V 3.3V	-
-	15	12.1 ELECTRICA ADD INPUT	L CHARACTERISTICS		
-	23~27	12.3.2 MODULE I	DEFECTS CALSSIFICATION THE ENTIRE PAGE		
	28	13.4.1 STANDAR NO.5 : ADD NO ITEM 6 THEEMAL SHOCK (NOT OPERATED) 7 (ELECTROSTATIC 1 (NOT OPERATED) 1 (ELECTROSTATIC 1 (NOT OPERATED) 2 (ELECTROSTATIC 2 (ELECTROSTATIC	D SPECIFICATIONS FOR RELIABIL D STORAGE DESCRIPTION THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 200 CYCLES OF OPERATION: 25°C FOR 30 MINUTES -+70°C FOR 30 MINUTES AIR DISCHARGE ± 4KV CONTACT DISCHARGE ± 2KV DESCRIPTION THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 25 CYCLES OF OPERATION: 20°C FOR 00 MINUTES -+70°C FOR 60 MINUTES AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 18KV	ITY OF LCD N	MODULE
-	30	ADD 13.6.5 NOT CAUSE ANY ME	ALLOWED TO INFLICT ANY EXTE CHANICAL INTERFERENCE ON TH E TAIL BENDING BACKWARDS!		
JUL.09,2008	3	4. ELECTRICAL PARAMETER SYMBOL COND VOLTAGE VIEW OF	CHARACTERISTICS MIN TYP MAX UNIT REMARK NIMA 192 20.4 21.6 V NOET(1) ↓		
	7	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	MIN. TYP. MAX. UNIT REMARK		
	9	7 . OUTLINE DIN MARK 🛆 : ADDI	MENSIONS NG TEXT AND NOTE.		

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	10	8. BLOCK DIMEN				
	21		fect : AQL 0.65% fect : AQL 1.0%	\rightarrow AQL 1.0		
NOV.21, 2008	7	COLOR OF CIE COORDINATE	ARACTERISTIC: M SYMBOL WHITE Wy M SYMBOL WHITE Wy	$\begin{array}{c c c c c c c c c c c c c c c c c c c $	86 0.336 11 0.361 P. MAX. 0.36	(6)
OCT.07, 2010	4	5.1 PIXEL TIMINO CHARACTEI PHASE DIFFEREN SIGNAL FALLI CHARACTEI PHASE DIFFEREN SIGNAL FALLI	RISTICS SYN ICE OF SYNC ING EDGE RISTICS SYN ICE OF SYNC ING EDGE t	IBOL MIN. TYP. MA hv 1 24 ↓ IBOL MIN. TYP. MA hv -4 +	0 tDOTCLK X. UNIT	
	6	DCLK	1 Potonia Pariod 2 double		arizontal Period etk 31	

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TECHNOLOGIES CORPORATION E T 0 3 5 0 0 9 D H 6 (RoHS) 5 1 I. GENERAL SPECIFICATIONS I.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO : H X 8 2 3 8 - A I.2 MATERIAL SAFETY DESCRIPTION ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE) 2. MECHANICAL SPECIFICATIONS 3.5 inch (2) NUMBER OF DOTS 320W * (RGB) * 240H DOTS (3) MODULE SIZE 76.8W * 63.8H * 4.4(D) mm (WITHOUT FPC) (4) EFFECTIVE AREA (4) EFFECTIVE AREA 72.2W*54.76H mm (LCD) 70.8W * 53.26H mm (LCD) 70.8W * 53.26H mm (LCD) 70.8W * 53.26H mm (6) DOT SIZE 0.073W * 0.219H mm (7) PIXEL SIZE 0.219W * 0.219H mm (8) LCD TYPE TFT, TRANSMISSIVE (9) COLOR 262K (10) VIEWING DIRECTION 60 °CLOCK (11) BACK LIGHT LED, COLOR : WHITE (12) INTERFACE MODE RGB 24 BIT, PARALLEL (DE/SYNC MODE)	EMERGING DISPLAY	MODEL NO.	VERSION	PAGE
1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO : H X 8 2 3 8 - A 1.2 MATERIAL SAFETY DESCRIPTION ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL S(PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE) 2. MECHANICAL SPECIFICATIONS (1) DIAGONALS (2) NUMBER OF DOTS (3) MODULE SIZE (4) EFFECTIVE AREA (7) SIZE (6) DOT SIZE (7) PIXEL SIZE (10) COLOR (20) COLOR (30) COLOR (41) DIACLIGHT (5) ACTIVE AREA (11) DIACK LIGHT (12) DIACTION (13) MODULE SIZE (14) EFFECTIVE AREA (15) ACTIVE AREA (17) PIXEL SIZE (17)	TECHNOLOGIES CORPORATION	E T 0 3 5 0 0 9 D H 6 (RoHS)	5	1
PLEASE REFER TO : H X 8 2 3 8 - A 1.2 MATERIAL SAFETY DESCRIPTION ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE) 2. MECHANICAL SPECIFICATIONS DIAGONALS Sinch NUMBER OF DOTS MODULE SIZE MODULE SIZE ACTIVE AREA Sinch ACTIVE AREA ONT SIZE ONT SIZE ONT SIZE OLIPHENNE LCD TYPE MACTIVE AREA DESCENTION OCLOCK BACK LIGHT 	1. GENERAL SPECIFICATIONS			
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(6) DOT SIZE 0.073W * 0.219H mm (7) PIXEL SIZE 0.219W * 0.219H mm (8) LCD TYPE TFT , TRANSMISSIVE (9) COLOR 262K (10) VIEWING DIRECTION 6 O'CLOCK (11) BACK LIGHT LED , COLOR : WHITE	(5) ACTIVE AREA	70.08W * 52.56H mm	(LCD)	
(7) PIXEL SIZE 0.219W * 0.219H mm (8) LCD TYPE TFT , TRANSMISSIVE (9) COLOR 262K (10) VIEWING DIRECTION 6 O'CLOCK (11) BACK LIGHT LED , COLOR : WHITE		70.8W * 53.26H mm (Г/Р)	
(8) LCD TYPETFT , TRANSMISSIVE(9) COLOR262K(10) VIEWING DIRECTION6 O'CLOCK(11) BACK LIGHTLED , COLOR : WHITE	(6) DOT SIZE	0.073W * 0.219H mn	1	
(9) COLOR 262K (10) VIEWING DIRECTION 6 O'CLOCK (11) BACK LIGHT LED, COLOR : WHITE	(7) PIXEL SIZE	0.219W * 0.219H mm		
(10) VIEWING DIRECTION 6 O'CLOCK (11) BACK LIGHT LED, COLOR : WHITE	(8) LCD TYPE	TFT , TRANSMISSIV	Е	
(11) BACK LIGHT LED, COLOR: WHITE	(9) COLOR	262K		
	(10) VIEWING DIRECTION	6 O'CLOCK		
(12) INTERFACE MODE RGB 24 BIT, PARALLEL (DE/SYNC MODE)	(11) BACK LIGHT	LED , COLOR : WHIT	ΓE	
	(12) INTERFACE MODE	RGB 24 BIT, PARALI	LEL (DE/SY	NC MODE)

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

PARAMETER	SYMBOL	MIN.	MAX.	UNIT	REMARK
POWER VOLTAGE	VDD	VSS-0.3	5.0	V	
TOWER VOLTAGE	VCC	-0.3	4.0	V	
LED BACKLIGHT DISSIPATION	PD		540	mW	
LED BACKLIGHT CURRENT	IF		25	mA	
LED BACKLIGHT REVERSE VOLTAGE	VR		30	V	

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPERA	ATING	STORAGE		REMARK
I I L IVI	MIN. MAX.		MIN.	MAX.	KEWAKK
AMBIENT TEMPERATURE	-10°C	60°C	-20°C	70°C	NOTE (1), (2)
HUMIDITY	NOTI	E(3)	NOT	E(3)	WITHOUT
HOWIDITT	NOT	L(3)	NOT	5(5)	CONDENSATION
VIBRATION		2.45m/s ² (0.25G)		11.76m/s ² (1.2G)	5~20Hz, 1HR 20~500Hz(20Hz), 1HR 20~500Hz(500Hz), 1HR X,Y,Z,TOTAL 3HR
SHOCK		29.4m/s ² (3G)		490m/s ² (50G)	10 m SECONDS XYZ DIRECTIONS 1 TIME EACH
CORROSIVE GAS	NOT ACCEPTABLE		NOT ACC	EPTABLE	

NOTE (1) : Ta AT -20°C : 48HR MAX.

70°C:168HR MAX.

NOTE (2) : BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT TEMPERATURE THIS PHENOMENON IS REVERSIBLE .

NOTE (3) : $Ta \le 60^{\circ}C$: 90%RH MAX (96HRS MAX).

Ta > 60°C : ABSOLUTE HUMIDITY MUST BE LOWER THAN THE HUMIDITY OF 90%RH AT 60°C(96HRS MAX).

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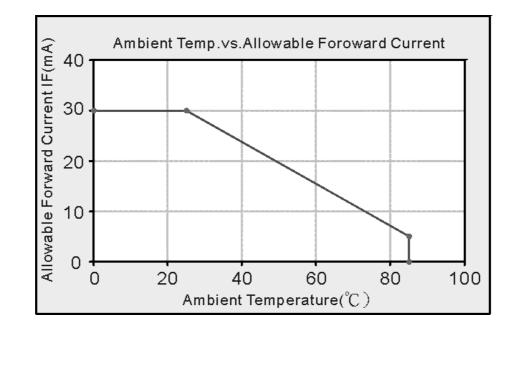
4. ELECTRICAL CHARACTERISTICS

							$Ta = 25 \circ C$
PARAMETER	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	REMARK
DIGITAL POWER SUPPLY	VCC	—	2.5	3.3	3.6	V	
ANALOG POWER SUPPLY	VDD		2.5	3.3	3.6	V	
DIGITAL OPERATING CURRENT	ICC			1	2	mA	
ANALOG OPERATING CURRENT	Idd			10	15	mA	
LOGIC HIGH OUTPUT VOLTAGE	VOH	IOUT=- 100μA	0.9*VCC		VCC	V	
LOGIC LOW OUTPUT VOLTAGE	VOL	IOUT=100µA	0		0.1*VCC	V	
LOGIC HIGH INPUT VOLTAGE	VIH	H LEVEL	0.8*VCC		VCC	V	
LOGIC LOW INPUT VOLTAGE	VIL	L LEVEL	0		0.2*VCC	V	
POWER SUPPLY FOR LED BACKLIGHT	V _F	IF=20mA	18	19.8	21.6	V	NOET(1)
LED LIFE TIME			30k	40k		hr	

NOTE (1): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT



NOTE (2): AMBIENT TEMP .VS. ALLOWABLE FORWARD CURRENT.(PER LED)



MODEL NO. VERSION PAGE EMERGING DISPLAY TECHNOLOGIES CORPORATION E T 0 3 5 0 0 9 D H 6 (RoHS) 5 4 5. TIMING CHART 5.1 PIXEL TIMING **CHARACTERISTICS** SYMBOL TYP. MIN. MAX. UNIT DCLK FREQUENCY **fDCLK** 6.5 10 MHz DCLK PERIOD tDCLK 100 154 ____ ns VERTICAL SYNC SETUP TIME 20 tvsys ____ ns VERTICAL SYNC HOLD TIME tvsyh 20 ns ____ ____ HORIZONTAL SYNC SETUP TIME thsys 20 ns _____ HORIZONTAL SYNC HOLD TIME tvsyh 20 ns ____ PHASE DIFFERENCE OF SYNC thv -4 +4tDCLK SIGNAL FALLING EDGE tCKL 50 DCLK LOW PERIOD

tCKH

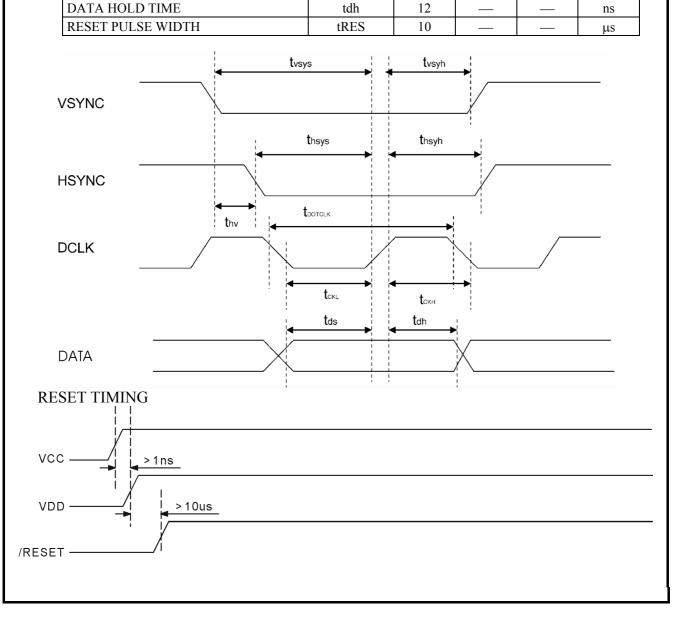
tds

50

12

DCLK HIGH PERIOD

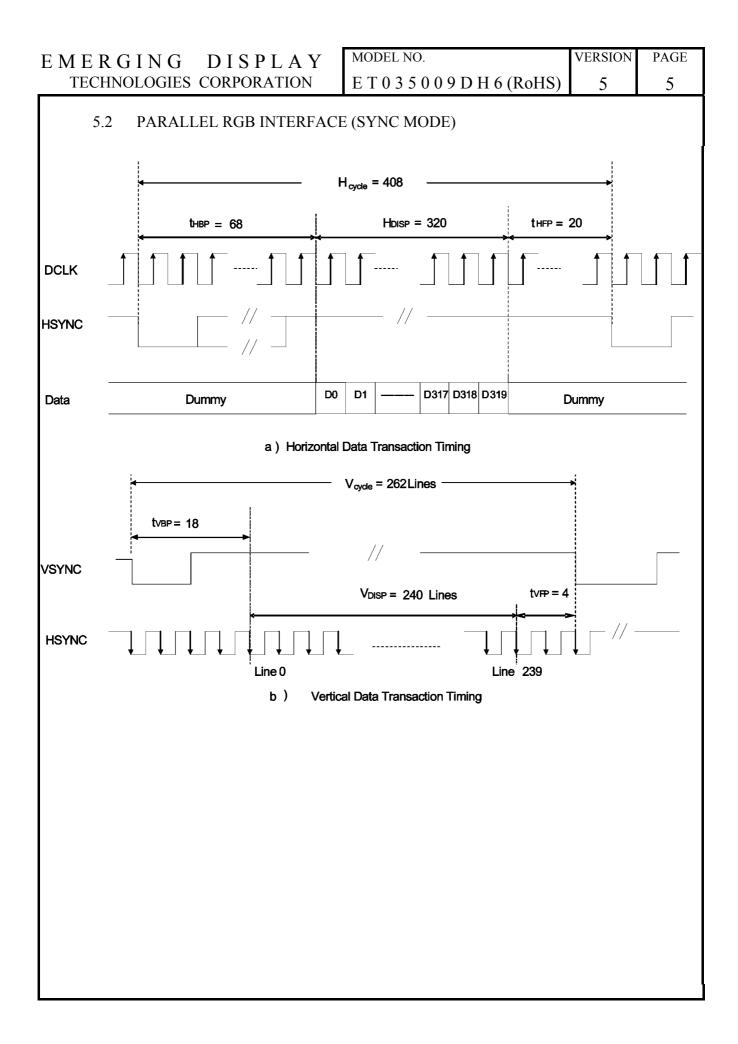
DATA SETUP TIME

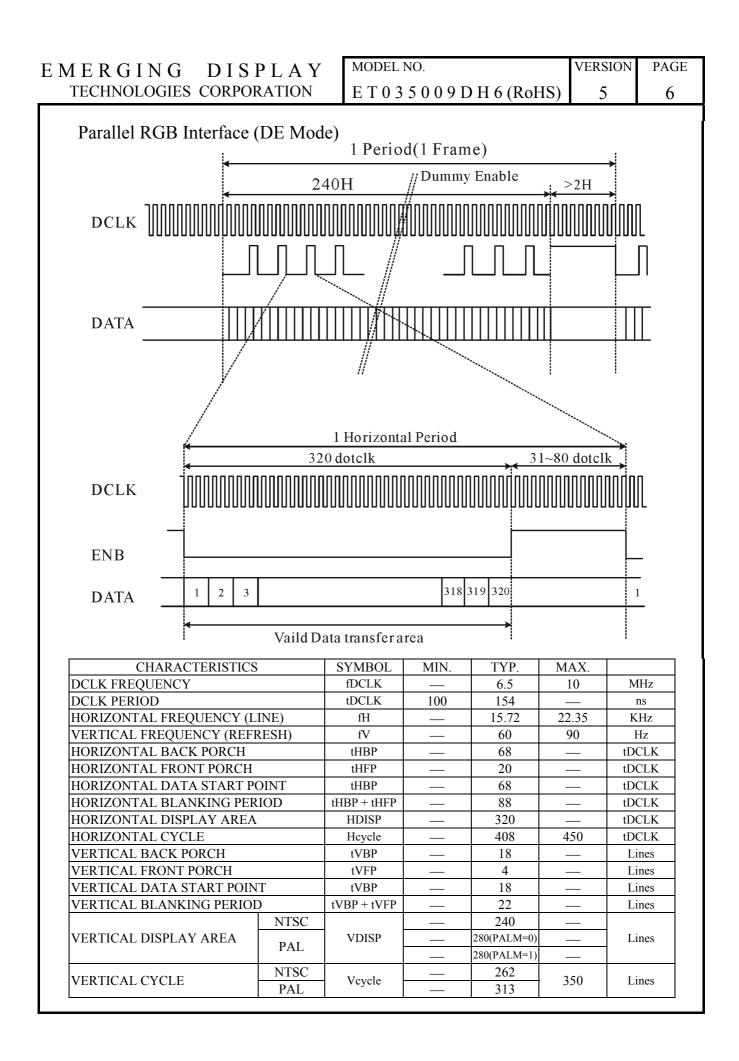


ns

ns

ns





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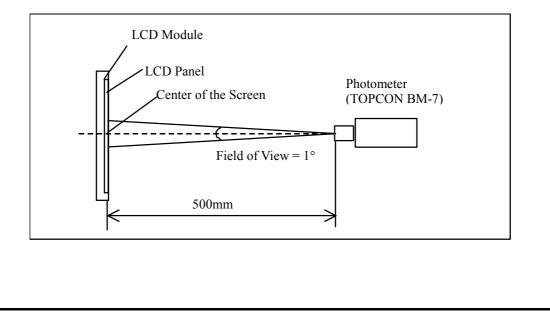
 $T_{2} = 2.5 \pm 20C$

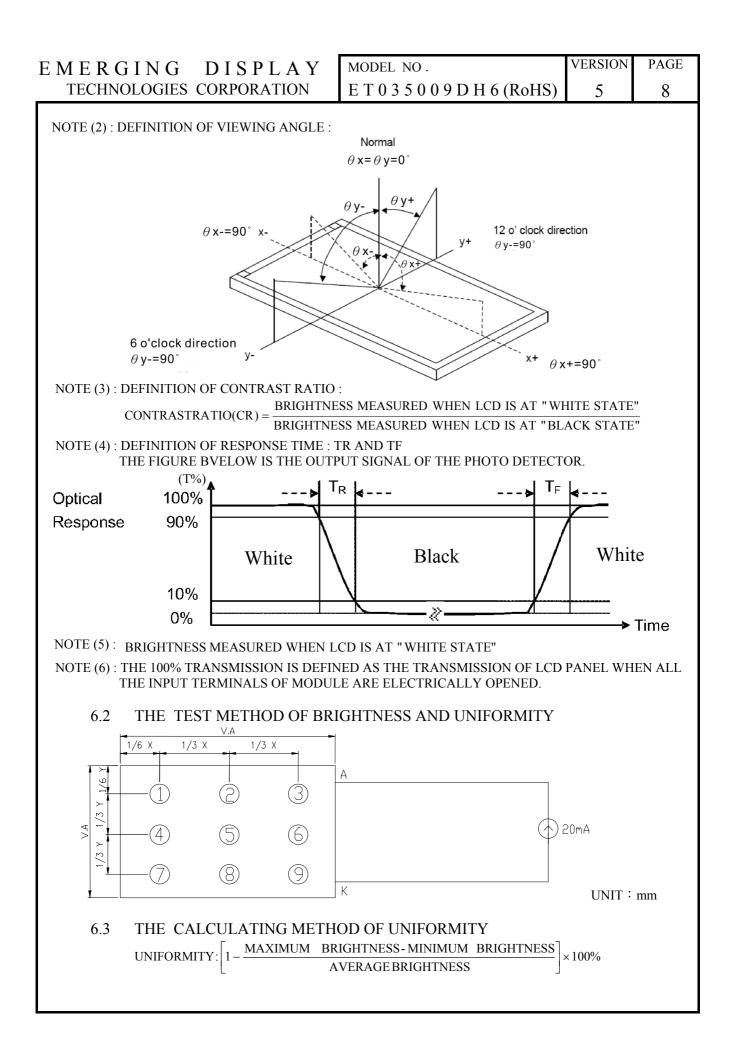
6. OPTICAL CHARACTERISTICS (NOTE1) 6.1 OPTICAL CHARACTERISTICS

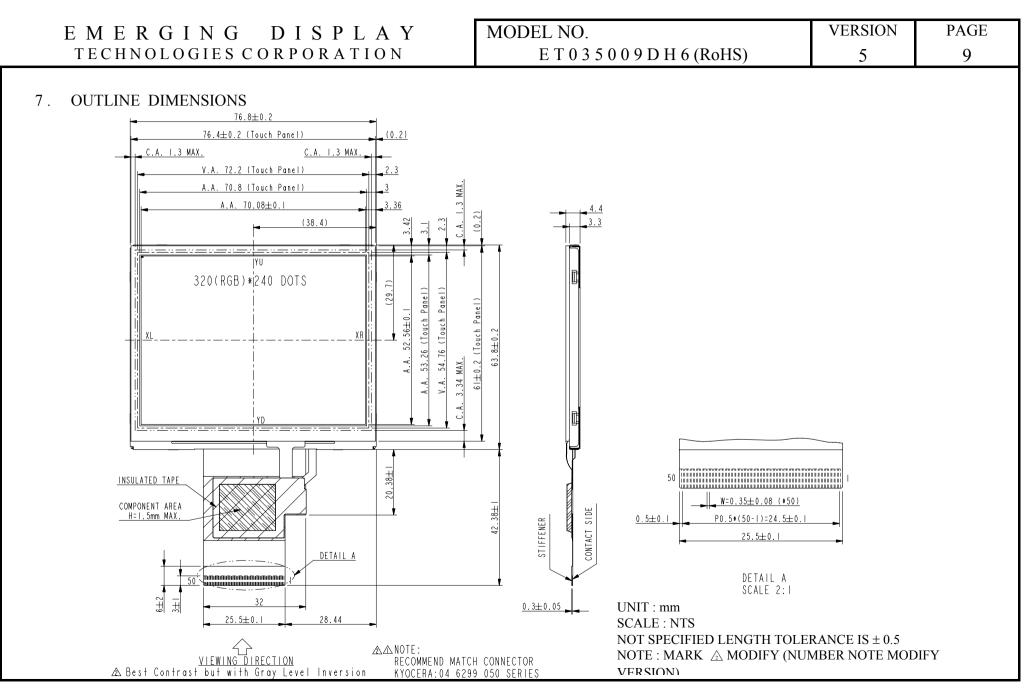
					-	-	-	18	$\mathbf{a} = 2.5 \pm 2^{\circ} \mathbf{C}$	
ITEI	M	SYMBOL	COND	ITION	MIN.	TYP.	MAX.	UNIT	REMARK	
		$\theta_{y^{+}}$		θ _x =0°	50	55				
VIEWING ANCI	VIEWING ANGLE		CD > 10	$\theta_x = 0^{-1}$	70	75		dag	(2) (2)	
VIEWING ANGL			$CR \ge 10$	0 -00	70	75		deg.	(2), (3)	
		θ_{x}		$\theta_y=0^{\circ}$	70	75				
CONTRAST RAT	IO	CR	$\theta x=0^{\circ}, \ \theta y=0^{\circ}$		300	400			(3)	
RESPONSE TIM	7	T _R (rise)	009	009		15	20	m 600	(4)	
KESPOINSE TIIVII	2	T _F (fall)	$\theta x=0^{\circ}, \ \theta y=0^{\circ}$			35	50	msec	(4)	
THE BRIGHTNESS OF MODULE		В	θx=0°, θy=0° IF=20mA		300	350	_	cd/m ²	(5)	
		Wx			0.26	0.31	0.36			
	WHITE	Wy			0.26	0.31	0.36			
	DED	Rx			0.562	0.612	0.662			
COLOR OF CIE	RED	Ry	θx=0°, θ IF=20n			0.305	0.355	0.405		(6)
COORDINATE	GREEN	Gx	NTSC		0.262	0.312	0.362			
	UKEEN	Gy			0.533	0.583	0.633			
	BLUE	Bx			0.090	0.140	0.190			
	DLUE	Ву			0.020	0.070	0.120			
THE UNIFORMI' BRIGHTNESS	ГҮ OF		_	_	70			%		

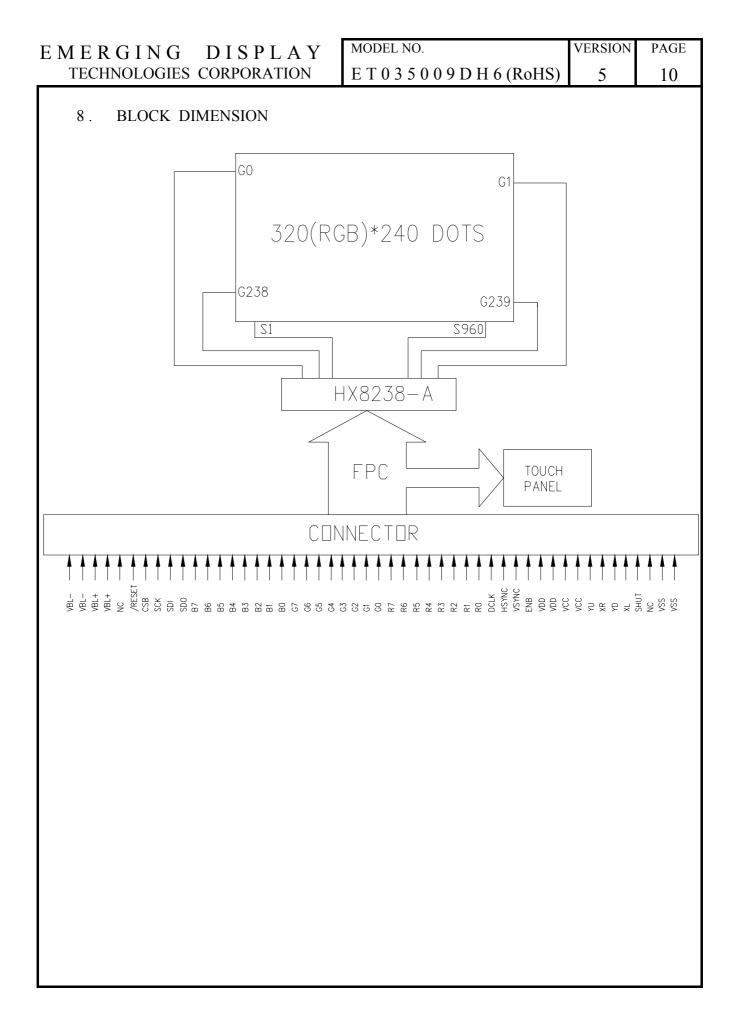
NOTE (1): TEST EQUIPMENT SETUP:

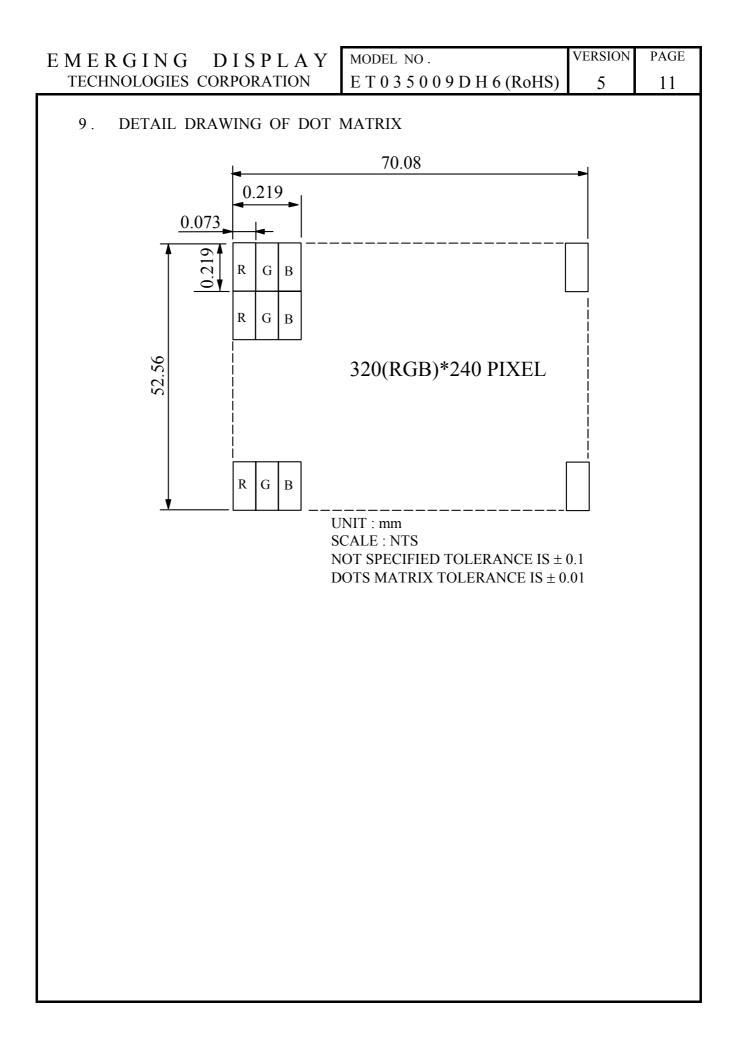
AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES, THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE, WINDLESS, AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7 (FAST) WITH A VIEWING ANGLE OF 1° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.











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10. INTERFACE SIGNALS

PIN NO	SYMBOL	I/O	FUNCTION
1	VBL-	Р	BACKLIGHT LED GROUND (K)
2	VBL-	Р	BACKLIGHT LED GROUND (K)
3	VBL+	Р	BACKLIGHT LED POWER (A)
4	VBL+	Р	BACKLIGHT LED POWER (A)
5	NC		NOT USE
6	/RESET	Ι	HARDWARE RESET
7	CSB	Ι	SPI INTERFACE CHIP SELECT BAR
8	SCK	Ι	SPI INTERFACE DATA CLOCK
9	SDI	Ι	SPI INTERFACE DATA (INPUT)
10	SDO	0	SPI INTERFACE DATA(OUTPUT)
11	B7	Ι	BLUE DATA BIT 7
12	B6	Ι	BLUE DATA BIT 6
13	B5	Ι	BLUE DATA BIT 5
14	B4	Ι	BLUE DATA BIT 4
15	B3	Ι	BLUE DATA BIT 3
16	B2	Ι	BLUE DATA BIT 2
17	B1	Ι	BLUE DATA BIT 1
18	B0	Ι	BLUE DATA BIT 0
19	G7	Ι	GREEN DATA BIT 7
20	G6	Ι	GREEN DATA BIT 6
21	G5	Ι	GREEN DATA BIT 5
22	G4	Ι	GREEN DATA BIT 4
23	G3	Ι	GREEN DATA BIT 3
24	G2	Ι	GREEN DATA BIT 2
25	G1	Ι	GREEN DATA BIT 1
26	G0	Ι	GREEN DATA BIT 0
27	R7	Ι	RED DATA BIT 7
28	R6	Ι	RED DATA BIT 6
29	R5	Ι	RED DATA BIT 5
30	R4	Ι	RED DATA BIT 4
31	R3	Ι	RED DATA BIT 3
32	R2	Ι	RED DATA BIT 2
33	R1	Ι	RED DATA BIT 1
34	R0	Ι	RED DATA BIT 0

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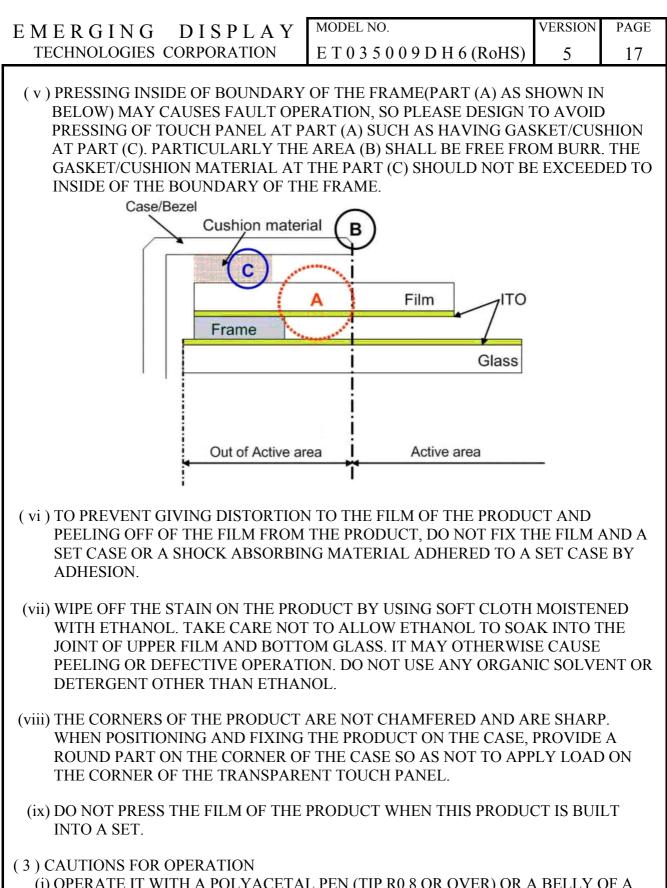
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PIN NO	SYMBOL	I/O	FUNCTION		
35	DCLK	Ι	DOT DATA COLCK		
36	HSYNC	Ι	HORIZONTAL SYNC INPUT		
37	VSYNC	Ι	VERTICAL SYNC INPUT	DE MODE : HSYNC, VSYNC, FLOATING SYNC MODE : DE FLOATING	
38	ENB	Ι	DATA ENABLE INPUT		
39	VDD	Р	ANALOG POWER		
40	VDD	Р	ANALOG POWER		
41	VCC	Р	DIGITAL POWER		
42	VCC	Р	DIGITAL POWER		
43	YU	Ι	TOP PANEL		
44	XR	Ι	RIGHT PANEL	TOUCH PANEL INTERFACE	
45	YD	Ι	BOTTOM PANEL	SIGNALS	
46	XL	Ι	LEFT PANEL		
47	SHUT	Ι	DISPLAY SHUT DOWN PIN TO PU CONNECT TO VCC FOR SLEEP MC CONNECT TO VSS FOR NORMAL (
48	NC		NOT USE		
49	VSS	Р	GROUND		
50	VSS	Р	GROUND		

	ING DISPLAY		VERSION	PAGE
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	WER SUPPLY POWER SUPPLY FOR LC	M		
1 1 .1				
	VDD			
	VCC			
		3.3V 3.3V		
	VSS	,		
	(A) VBL+	20mA		
	(K) VBL-			
	TOUCH PANEL			

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12. TOUCH PANEL SPECIFICATI 12.1 ELECTRICAL CHARACTER		CS		Ta = 2	25°C
ITEM		NDITION	SPEC.	UNIT	
LINEARITY	00	_	1.5	%	
	2	X AXIS	200~900	0	
TERMINAL RESISTANCE	Y	Y AXIS	$200 \sim 900$	Ω	
INSULATION RESISTANCE]	DC25V	20	MΩ	
INPUT VOLTAGE			5(TYP.)	V	
12.2.1 PURPOSE : IN ORDER TO PREVENT ACC DETERIORATION, PLEASE KI INHIBITED POINTS. 12.2.2 ITEM AND ILLUSTRATIO (1) STRUCTURE, AREA DEFI THE STRUCTURE AND TH TOUCH PANEL ARE DEFI Case/Bezel Cushion ma Area (d) Non-Active area	EEP T NN : NITIO HE PE NED aterial	THE FOLLO	VWING PRECAUTIO	ONS AND	ΉIS
Transparent insulatio	n area		(a): /e area (A/A)		
		alayer ITO film: M layer ITO film: M			
THE ABOVE FIGURE IS OUR D IF IT CANNOT MEET YOUR RE ENGINEERS FOR FURTHER DI ABOVE FIGURE ILLUSTRATES DESIGN. IN ORDER TO PREVE UNUSUAL PERFORMANCE DE PANEL, PLEASE CARRY OUT CASE DESIGNING AND A TOU CONSIDERING THE DEFINITION FIGURE.	EQUI ISCUS S THI ENT EGRA THE S JCH P	REMENT, F SSION. E RECOMM DATION A SET PANEL ASS	PLEASE CONTACT IENDED BEZEL AN ND MALFUNCTIO EMBLING METHO	ND CUSHION N OF A TOU D AFTER SU	СН

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TECHNOLOGIES CORPORATION	E T 0 3 5 0 0 9 D H 6 (RoHS)	5	16
PRECISION, OPERATION FO STRONGLY RECOMMENDE MENU KEYS WITHIN THE A	RANTEED THE POSITION DATA RCE AND OTHER OPERATIONS D TO PLACE THE OPERATION I CTIVE AREA. DUE TO STRUCT ABLE AT THE EDGE OR CLOSE	S. IT IS BUTTON (URE, THE	OR
ITS FUNCTION. WHEN THIS DEGRADATION OF ITS PERF SLIDING DURABILITY BECC THE ACTIVE AREA (AREA-(A OPERATION FORCE REQUIR	EED AREA RANTEE A TOUCH PANEL OPEI AREA IS PRESSED, TOUCH PAN FORMANCE AND DURABILITY OMES ABOUT ONE-TENTH COM A) AS GUARANTEED AREA) AN ES ABOUT DOUBLE. ABOUT 0. E ACTIVE AREA CORRESPOND	NEL SHOV SUCH AS IPARED V ND ITS 5 MM OU'	VS A PEN VITH TSIDE
APPLIED TO A TRANSPARE	EA 5 PRESSING, BECAUSE AN EXC NT ELECTRODE (ITO) AND A S EL FUNCTION BY PRESSING.		
AREA(d) : NON-ACTIVE AREA THE AREA DOES NOT ACTIV	ATE EVEN IF PRESSED.		
(2) CAUTIONS FOR INSTALLING AND A (i) DO NOT GIVE EXCESSIVE STRAIN			
(ii) FLEXIBLE PATTERN CABLE IS CO PRESSURE METHOD. SO, DO NOT PATTERN. DO NOT ADD AN EXCE MAKES PEELING OFF OF THE FPC OR MOUNT ANY ADDITIONAL GO FILM/PLATE ON THE FPC, BECAU STRESS AT THE FPC BONDING AR OF FPC WITH TOUCH PANEL.	APPLY EXCESSIVE FORCES TO ESSIVE FORCE TO A FPC(FLEX FROM THE PRODUCT. DO NOT OODS ON THE FPC SUCH AS AD SE SUCH ADDITIONAL GOODS	D THE FLI TAIL) THA F FIX, ADI DITIONA WILL AP	EXIBLE AT HERE L PLY A
(iii) IN ORDER NOT TO APPLY LOAD AT LEAST 0.3MM BETWEEN THE		CLEARAN	ICE OF
(iv) WE RECOMMEND THE DESIGN O BOUNDARY OF THE ACTIVE ARE OPERATION AT OUTSIDE OF THE THE FUNCTION OR DURABILITY DEFINITION). BEZEL'S EDGE PART MAY GUIDI REPEATEDLY. IF THE BEZEL IS P	EA INSIDE IN ORDER TO PREVE E ACTIVE AREA WHICH CAN NO (REFER TO ITEM 5.1.2. STRUCT E THE PEN SLIDING ON THE SA	ENT AN OT GUAR FURE, ARI ME POSI	ANTEE EA ΓΙΟΝ
	LACED OUTSIDE OF THE ACTI		



(i) OPERATE IT WITH A POLYACETAL PEN (TIP R0.8 OR OVER) OR A BELLY OF A FINGER WITHOUT APPLYING EXCESSIVE LOAD. NEVER USE ANY MECHANICAL PENCILS, BALL POINT PENS AND HARD FINGERTIPS WHOSE TIP IS HARD FOR INPUT, OTHERWISE MALFUNCTIONS MAY RESULT.

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(ii) THE INPUT POSITION MAY BE FLU USE. IT IS DESIRABLE TO PROVID A CIRCUIT AND SOFTWARE.			
(iii) OPERATION AT THE OUT OF ACTI CAUSES A SERIOUS DAMAGE OF OPERATE AT THE OUT OF ACTIVE	A TRANSPARENT ELECTRODE		IT
(iv) IN CASE OF CLEANING THE PART SET, USE A SOFT CLOTH WITH A I CLEAN WITH A THI NG OTHER TH EDGES LIKE A FINGER NAIL ETC. TRANSPARENT CONDUCTIVE FILL TO YOUR LAST CUSTOMERS.	FINGER BERRY OR A COTTON IAN THE FINGER SUCH AS HAP ON THE CLOTH, BECAUSE IT (BUD. DO 1 RD OR SHA CAUSE	NOT ARP
12.3 DURABILITY			
12.3.1 STYLUS HITTING : ONE MILLION TIMES OR OVER NO DAMAGE ON FILM SURFAG PEN : R8 mm SILICON RUBBER LOAD : 250g FREQUENCY : 240 times/min MEASUREMENT POSITION: 1 POINT OF TOUCH PANEL AC REPEATED : OVER 1,000,000 T	CE X CTIVE AREA		
12.3.2 PEN TOUCH SLIDING DURAB 100,000 TIMES OR OVER WRITING WITH R0.8mm PLAST IN ACTIVE AREA. SPEED IS 60mm/sec.	ILITY : FIC STYLUS PEN; WRITING FO	RCE 150g	

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E M E R G I N G D I S P L A Y TECHNOLOGIES CORPORATION	E T 0 3 5 0 0 9 D H 6 (RoHS)	5	19
ON TOUCH PANEL SURFAC	LL FROM THE HEIGHT OF 30cm CE, MUST PASS BELOW CONDI ARANCE WITHOUT ANY CHANG	n AND FA TIONS:	LLING
HIGHT: 30cm HIGHT: 30cm 5cm 5cm 12.5 APPEARANCE INSPECTION PURPOSE : TO ESTABLISH APPEARANC QUALITY • SCOPE : TOUCH PANEL VIEW AREA	CE STANDARD AND MAINTAIN	- N PRODUC	CT
(PANEL MUST BI (C) VISUAL ANGEL : > 6 (D) LIGHT SOURCE : FL 12.5.2 JUDGE CRITERION : JUDGEMENT UNDER AB TESTED UNDER LIGHT T TESTING GOODS DEFEC WHICH WILL BE JUDGED SAMPLING STANDARD	UMINANCE : 500 LUX ° N HUMAN EYES AND PANEL : E TESTED UNDER LIGHT TRAN 50° ° LUORESCENT LIGHT SOURCE ° BOVE MENTIONED CRITERION TRANSPARENT), T CAN BE VISIBLE WITHIN 10 D AS MAJOR DEFECTS °	ISPARENT (PANEL N SECONDS	ÍUST BE

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INSPECTION ITEMS	SEPC.		JUDGE CRITERION	OPERATION GUIDELINE
SCRATCH	$W \le 0.1$ mm & L ≤ 10 mm		ACCEPTABLE	BACK GROUND
SCRATCH	W > 0.1n	nm or L > 10mm	NOT ACCEPTABLE	TESTING GOODS
LINEAR FOREIGN	$W \leq 0.1$	mm & L ≤5mm	ACCEPTABLE	
OBJECT	W > 0.1mm or L >5mm		NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux REFL
GRANULAR FOREIGN	D ≤ 0.3mm		ACCEPTABLE	FLUORESCENT LIGHT SOURCE
OBJECT	D >0.3mm		NOT ACCEPTABLE	60° ENVIRONMENTAL IUMINANCE : 500Lux
PET BUBBLES	D ≤0.6mm		ACCEPTABLE	
	D	>0.6mm	NOT ACCEPTABLE	
CHIP ON GLASS	CORNER	$X \le 3mm \land$ Y \le 3mm \cdot Z < t (t = /thickness)	ACCEPTABLE	Chip of glass
	EDGE	$W \le 3mm \cdot Y \le 3mm \cdot Z < t$		Y X X

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	L10330	0 9 D 11 0 (R0115)	5	21
1 3 . INSPECTION CRITERION 13.1 APPLICATION				
THIS INSPECTION STANDARD I DELIVERED FROM EMERGING I CUSTOMERS				ТО
13.2 INSPECTION CONDITIONS				
13.2.1 (1)OBSERVATION DIST (2)VIEW ANGLE : NON-OPERATION CO (PERPENDICULAR T OPERATION CONDIT (PERPENDICULAR T	ONDITION : : Ο LCD PANE ΓΙΟΝ : ±45°	±5° EL SURFACE)		
(PERPENDICULAR T		L SURFACE)		
Non-operation Condition		eration Condition 30cm~40cm		
	90°			
13.2.2 ENVIRONMENT CONDI AMBIENT TEMPERATU		20°C~25°C		
AMBIENT HUMIDITY		65±20%RH		
AMBIENI	C INSPECTION	MORE THAN 600L	ux	
ILLUMINATION FUNC	CTIONAL ECTION	300~500 Lux		
13.2.3 INSPECTION LOT QUANTITY PER DELIV	·	OR EACH MODEL		
13.2.4 INSPECTION METHOD A SAMPLING INSPECT FOLLOWING PROVISIO (a)APPLICABLE STANI MIL-STD-105E NORMAL INSPECTIO Level II (b)AQL : MAJOR DEFEO MINOR DEFEO	ION SHALL DNS TO JUD DARD : DN, SINGLE S CT : AQL 0.63	GE THE ACCEPTAE SAMPLING		ΉE

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13.3 INSPECTION STANDARDS

13.3.1 VISUAL DEFECTS CLASSIFICATION

TYPE OF DEFECT	INSPECTION ITEM	DEFECT FEATURE	AQL
	1.DISPLAY ON	• DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC	0.65
MAJOR DEFECT	2.BACKLIGHT	 NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION 	0.65
	3.DIMENSIONS	• SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS	
	1.DISPLAY ZONE	 BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED 	
MINOR DEFECT	2.BEZEL ZONE	• STAINS • SCRATCHES • FOREIGN MATTER	1.0
	3.SOLDERING	 INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS 	
	4.DISPLAY ON (ALL ON)	• LIGHT LINE	1

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13.3.2 MODULE DEFECTS CALSSIFICATION

	ITEM			ΓERIA	
1.	DISPLAY ON INSPECTION	 (1)INCORRECT PATTERN (2)MISSING SEGMENT (3)DIM SEGMENT (4)OPERATING VOLTAGE BEYOND SPEC 			
2.	OVERALL DIMENSIONS	(1)OVERALL DIM	(1)OVERALL DIMENSION BEYOND SPEC		
3.	DOT DEFECT	AND BLUE SC (2) BRIGHT DOT DARK DOT TOAL BRIGHT NOTE : 1. THE DEFINITIC THE SIZE OF A REGARDED AS 2. BRIGHT DOT : DOTS APPEAR PANEL IS DISP 3. DARK DOT : DOTS APPEAR	CREENS. ITEMS FAND DARK DOTS ON OF DOT : DEFECTIVE DOT S ONE DEFECTIVE BRIGHT AND UNCE DARK AND UNCE	CHANGED IN SIZE IN BLACK PATTERN. IANGED IN SIZE IN V	NT E DOT IS N WHICH LCI WHICH LCD
4.	FOREIGN BLACK/WHITE/ BRIGHT LINE/ SCRATCH OF VIEWING AREA	$\begin{tabular}{ c c c c c } $LENGTH : L$ \\ $L \le 0.3$ \\ \hline $0.3 < L \le 2.5$ \\ \hline $2.5 < L$ \\ \hline \end{tabular}$	WIDTH : W $W \le 0.05$ $0.05 < W \le 0.1$ $0.1 < W$	URE RED, GREEN, B PERMISSIBLE NO. IGNORE 4 NONE	
	FOREIGN MATTER \	WIDTH : W mm, LENGH : L mm AVERAGE DIAMETER (mm): D $D \le 0.15$ $0.15 < D \le 0.5$ 0.5 < D NOTE : DIAMETER D=(a+b)/2 b		NUMBER OF PIECES IGNORI 4 NONE	

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NO.	ITEM	CRITERIA			
			AVERAGE DIAMETER (mm): D	NUMBER OF PIECES PERMITTED	
			D ≤ 0.25	IGNORE	
		BUBBLE ON THE	0.25 < D ≤ 0.5	N ≤ 5	
		POLARIZER	0.5 < D	NOTE	
		SURFACE STATUS	D < 0.1 mm	IGNORE	
			$0.1 < D \le 0.3 mm$	N ≤ 3	
			D < 0.1 mm	IGNORE	
		CF FAIL / SPOT	$0.1 < D \le 0.3 mm$	N ≤ 3	
6.	DIRTICE FAIL SURFACE STAINS (2)THE EXTRANEOUS SUBSTANCE IS DEFINED AS OBSERVED WHEN THE MODULE IS POWER ON. (3)THE DEFINITION OF AVERAGE DIAMETER, D IS AS FOLLOWING. AVERAGE DIAMETER (D)=(a+b)/2 b		DEFECT OF POLARIZER HE POLARIZER BUBBLE CTIVE DISPLAY AREA. S DEFINED AS IT CAN BI IS POWER ON.		
7.	LINE DEFECT ON DISPLAY	OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW			
8.	MURA ON DISPLAY	IT'S OK IF MURA IS	IT'S OK IF MURA IS SLIGHT VISIBLE THROUNG 6% ND FILTER		
9.	UNEVEN COLOR SPREAD, COLORATION	(1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE.			
10.	BEZEL APPEARANCE	 (1)BEZEL MAY NOT HAVE RUST, BE DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS. 			
11	РСВ	 (2) BEZEE MOST COMPET WITH JOB STECHTCATIONS. (1) THERE MAY NOT BE MORE THAN 2mm OF SEALANT OUTSIDE THE SEAL AREA ON THE PCB, AND THERE SHOULD BE NO MORE THAN THREE PLACES. (2) NO OXIDATION OR CONTAMINATION PCB TERMINALS. (3) PARTS ON PCB MUST BE THE SAME AS ON THE PRODUCTION CHARACTERISTIC CHART. THERE SHOULD BE NO WRONG PARTS, MISSING PARTS OR EXCESS PARTS. (4) THE JUMPER ON THE PCB SHOULD CONFORM TO THE PRODUCT CHARACTERISTIC CHART. (5) IF SOLDER GETS ON BEZEL TAB PADS, LED PAD, ZEBRA PAD OR SCREW HOLD PAD, MAKE SURE IT IS SMOOTHED DOWN. 			

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NO. ITEM	CRITERIA
	 (1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD
	SOLDER FILLET
	(b)CHIP COMPONENT • SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING
	SOLDER FILLET
12. SOLDERING	• SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED
	SOLDER
	(3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE

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NO.	ITEM	CRITERIA
12.	SOLDERING	(b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE
		 (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. (5)NO COLD SOLDER JOINTS, MISSING SOLDER CONNECTIONS, OXIDATION OR ICICLE. (6)NO RESIDUE OR SOLDER BALLS ON PCB. (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB.
13.	BACKLIGHT	 (1)NO LIGHT (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT MUST BE JUDGEE USING LCD SPOT, LINES AND CONTAMINATION STANDARDS. (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG.
14.	GENERAL APPEARANCE	 (1)NO OXIDATION, CONTAMINATION, CURVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER BALLS ON PRODUCT. (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON THE INTERFACE PIN MUST BE PRESENT OR LOOK AS IF IT CAUSE THE INTERFACE PIN TO SEVER. (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING (COMPONENT OR CHIP COMPONENT) IS NOT BURNED INTO BROWN OR BLACK COLOR. (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT HARDENED. (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. (9)LCD PIN LOOSE OR MISSING PINS. (10)PRODUCT PACKAGING MUST THE SAME AS SPECIFIED ON PACKAGING SPECIFICATION SHEET. (11)PRODUCT DIMENSION AND STRUCTURE MUST CONFORM TO PRODUCT SPECIFICATION SHEET. (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT ADMIT ANY DIRT AND BREAK.

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NO.	ITEM		CRITERIA
		GENERAL GLASS CHIP :	CRACK IS NOT ACCEPTABLEabc $\leq t/2$ $<$ VIEWING AREA $\leq 1/8X$ $t/2 > , \leq 2t$ $\leq W/2$ $\leq 1/8X$ *W=DISTANCE BETWEEN SEALANT AREA AND LCD PANEL EDGE X = LCD SIDE LENGTH t = GLASS THICKNESS
15. 0	CRACKED GLASS	CORNER PART : b c c a c c a c c b c c a c c a c c a c c a c c a c c a c c a c c a c c c c c c c c c c c c c	$\begin{tabular}{ c c c c c c } \hline a & b & c \\ \hline \leq t/2 & < VIEWING AREA & \leq 1/8X \\ \hline > t/2 & , \leq 2t & \leq W/2 & < 1/8X \\ \hline *W=DISTANCE BETWEEN \\ SEALANT AREA AND LCD \\ PANEL EDGE \\ X = LCD SIDE LENGTH \\ t = GLASS THICKNESS \\ \hline \hline a & b & c \\ \hline \leq t & \leq 0.5mm & \leq 1/8X \\ \hline * X=LCD SIDE WIDTH \\ t = GLASS THICKNESS \\ \hline \end{tabular}$
			a b c ≤t ≤1/8X ≤L *X=LCD SIDE WIDTH t = GLASS THICKNESS L=ELECTRODE PAD LENGTH ©IF GLASS CHIPPING THE ITO TERMINAL, OVER 2/3 OF THE ITO MU REMAIN AND BE, INSPECTED ACCORDING TO ELECTRODE TERMINAL SPECIFICATIONS ©IF THE PRODUCT WILL BE HEAT SEALED BY THE CUSTOMER, THE ALIGNMENT MARK MUST NOT BE DEMAGED

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13.4 RELIABILITY TEST

13.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

NO	ITEM	DESCRIPTION
1	HIGH TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +60°C FOR 240 HRS
2	LOW TEMPERATURE OPERATION	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -10°C FOR 240 HRS
3	HIGH TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT +70°C FOR 240 HRS
4	LOW TEMPERATURE STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT -20°C FOR 240 HRS
5	HIGH TEMP / HUMIDITY TEST STORAGE	THE SAMPLE SHOULD BE ALLOWED TO STAND AT 60°C , 90% RH 240 HRS
6	THERMAL SHOCK (NOT OPERATED)	THE SAMPLE SHOULD BE ALLOWED TO STAND THE FOLLOWING 25 CYCLES OF OPERATION: -20°C FOR 60 MINUTES ~ +70°C FOR 60 MINUTES
7		AIR DISCHARGE ± 12KV CONTACT DISCHARGE ± 8KV

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE OCCURRED.

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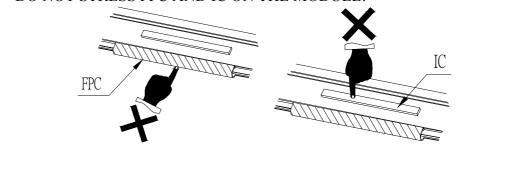
13.5 TESTING CONDITIONS AND INSPECTION CRITERIA

FOR THE FINAL TEST THE TESTING SAMPLE MUST BE STORED AT ROOM TEMPERATURE FOR 24 HOURS, AFTER THE TESTS LISTED IN TABLE 12.5, STANDARD SPECIFICATIONS FOR RELIABILITY HAVE BEEN EXECUTED IN ORDER TO ENSURE STABILITY.

NO	ITEM	TEST MODEL	INSPECTION CRITERIA
1	CURRENT CONSUMPTION	REFER TO SPECIFICATION	THE CURRENT CONSUMPTION SHOULD CONFORM TO THE PRODUCT SPECIFICATION.
2	CONTRAST	REFER TO SPECIFICATION	AFTER THE TESTS HAVE BEEN EXECUTED, THE CONTRAST MUST BE LARGER THAN HALF OF ITS INITIAL VALUE PRIOR TO THE TESTS.
3	APPEARANCE	VISUAL INSPECTION	DEFECT FREE

13.6 OPERATION

- 13.6.1 DO NOT CONNECT OR DISCONNECT MODULES TO OR FROM THE MAIN SYSTEM WHILE POWER IS BEING SUPPLIED .
- 13.6.2 USE THE MODULE WITHIN SPECIFIED TEMPERATURE ; LOWER TEMPERATURE CAUSES THE RETARDATION OF BLINKING SPEED OF THE DISPLAY ; HIGHER TEMPERATURE MAKES OVERALL DISPLAY DISCOLOR . WHEN THE TEMPERATURE RETURNS TO NORMALITY, THE DISPLAY WILL OPERATE NORMALLY .
- 13.6.3 ADJUST THE LC DRIVING VOLTAGE TO OBTAIN THE OPTIMUM CONTRAST .
- 13.6.4 POWER ON SEQUENCE INPUT SIGNALS SHOULD NOT BE SUPPLIED TO LCD MODULE BEFORE POWER SUPPLY VOLTAGE IS APPLIED AND REACHES THE SPECIFIED VALUE. IF ABOVE SEQUENCE IS NOT FOLLOWED, CMOS LSIS OF LCD MODULES MAY BE DAMAGED DUE TO LATCH - UP PROBLEM.
- 13.6.5 NOT ALLOWED TO INFLICT ANY EXTERNAL STRESS AND TO CAUSE ANY MECHANICAL INTERFERENCE ON THE BENDING AREA OF FPC DURING THE TAIL BENDING BACKWARDS! DO NOT STRESS FPC AND IC ON THE MODULE!



13.7 NOTICE

- 13.7.1 USE A GROUNDED SOLDERING IRON WHEN SOLDERING CONNECTOR I/O TERMINALS . FOR SOLDERING OR REPAIRING, TAKE PRECAUTION AGAINST THE TEMPERATURE OF THE SOLDERING IRON AND THE SOLDERING TIME TO PREVENT PEELING OFF THE THROUGH-HOLE-PAD .
- 13.7.2 DO NOT DISASSEMBLE . EDT SHALL NOT BE HELD RESPONSIBLE IF THE MODULE IS DISASSEMBLED AND UPON THE REASSEMBLY THE MODULE FAILED .
- 13.7.3 DO NOT CHARGE STATIC ELECTRICITY , AS THE CIRCUIT OF THIS MODULE CONTAINS CMOS LSIS. A WORKMAN'S BODY SHOULD ALWAYS BE STATIC-PROTECTED BY USE OF AN ESD STRAP. WORKING CLOTHES FOR SUCH PERSONNEL SHOULD BE OF STATIC -PROTECTED MATERIAL .
- 13.7.4 ALWAYS GROUND THE ELECTRICALLY-POWERED DRIVER BEFORE USING IT TO INSTALL THE LCD MODULE. WHILE CLEANING THE WORK STATION BY VACUUM CLEANER, DO NOT BRING THE SUCKING MOUTH NEAR THE MODULE ; STATIC ELECTRICITY OF THE ELECTRICALLY-POWERED DRIVER OR THE VACUUM CLEANER MAY DESTROY THE MODULE .
- 13.7.5 DON'T GIVE EXTERNAL SHOCK.
- 13.7.6 DON'T APPLY EXCESSIVE FORCE ON THE SURFACE.
- 13.7.7 LIQUID IN LCD IS HAZARDOUS SUBSTANCE. MUST NOT LICK AND SWALLOW. WHEN THE LIQUID IS ATTACH TO YOUR, SKIN, CLOTH ETC. WASH IT OUT THOROUGHLY AND IMMEDIATELY.
- 13.7.8 DON'T OPERATE IT ABOVE THE ABSOLUTE MAXIMUM RATING.
- 13.7.9 STORAGE IN A CLEAN ENVIRONMENT, FREE FROM DUST, ACTIVE GAS, AND SOLVENT.
- 13.7.10 STORE WITHOUT ANY PHYSICAL LOAD.
- 13.7.11 REWIRING: NO MORE THAN 3 TIMES.